

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(design adj vector) and (binding adj header) and (binding adj trailer) and (virtual adj machine) and (instruction adj set)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:22
L2	1	(design adj vector) and header and trailer and (virtual adj machine) and (instruction adj set)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:22
L3	1	(design adj vector) and (virtual adj machine) and (instruction adj set)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:23
L4	239	vector and (virtual adj machine) and (instruction adj set)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:23
L5	14	4 and header and trailer	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:24

L6	13	5 not 1	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:25
L7	167	instruction adj processor adj system	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:29
L8	167	7 and (design or processor or header or trailer or vitrual or vector)	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:31
L9	0	(instruction adj processor adj system) with (design or develop or create)	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:30
L10	103	(instruction adj processor) with (design or develop or create)	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:30

L11	103	10 and (design or processor or header or trailer or vitrual or vector)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:33
L12	6	("5937193" "5953741" "6002874" "6076141" "6085307" "6477702").PN.	US-PGPU B; USPAT; USOCR	OR	ON	2005/10/28 11:33
L13	6	12 and (design or processor or header or trailer or vitrual or vector)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:42
L14	2159	(instruction adj processor).ti,ab.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:48
L15	2159	14 and (design or processor or header or trailer or vitrual or vector)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:44
L16	257	15 and (design or develop)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TD B	OR	ON	2005/10/28 11:44

L17	257	16 and (design or develop or processor or header or trailer or vitrual or vector)	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:49
L18	417	(instruction adj processor).ti.	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:48
L19	417	18 and (design or develop or processor or header or trailer or vitrual or vector)	USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2005/10/28 11:49